



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 09/868,048

Confirmation No.: 5497

In re Application of:

Mana HAMADA et al.

Group Art Unit: 2183

Filed: July 16, 2001

Examiner: Daniel H. Pan

For: CONDITIONAL VECTOR ARITHMETIC METHOD
AND CONDITIONAL VECTOR ARITHMETIC UNIT

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR § 1.56, the attention of the U.S. Patent and Trademark Office is hereby directed to the references listed on the attached Form PTO-1449. Copies of each of the references listed on Form PTO-1449 are attached.

Also enclosed is a copy of a European Search Report bearing a mailing date of October 22, 2004, and the USPTO is directed thereto for a concise statement of possible relevance of the references cited therein.


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The above information is presented so that the U.S. Patent and Trademark Office may, in the first instance, determine any materiality thereof to the claimed invention. See 37 CFR § 1.104(a) and § 1.106(b) concerning the USPTO duty to consider and use any such information. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that these references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

November 30, 2004
Date



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Registration No. 25,177

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Attorney Docket No.: HYAE:120

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FORM PTO 1449 (modified)

U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICELIST OF REFERENCES CITED BY APPLICANT(S)
(Use several sheets if necessary)ATTY DOCKET NO.
HYAE:120SERIAL NO.
09/868,048

NOV 30 2004

APPLICANT
Mana HAMADA et al.FILING DATE
July 16, 2001GROUP
2183

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	4,128,880	12/1978	Cray, Jr.	364	200	06/1976

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION NO	TRANSLATION YES

OTHER DOCUMENT(S) (Including Author, Title, Date, Pertinent Pages, Etc.)

T. Araki et al., "The Architecture of a Vector Digital Signal Processor for Video Coding," Digital Signal Processing 2, Estimation, VLSI. San Francisco,
March 23, 1992, vol. 5 Conf. 17, pp. 681-684.

J. Hennessy et al., "Computer Architecture - A Quantative Approach," Morgan Kaufmann, XP002297069, 1990, pp. 251-252.

M. Toyokura et al., "A Video Digital Signal Processor with a Vector-Pipeline Architecture," IEEE International Solid-State Circuits Conference,
New York, February 1992, XP000315769, ISSN: 0193-6530, Vol. 35, pp. 72-72, 248

K. Aono et al., "A Video Digital Signal Processor with a Vector-Pipeline Architecture," IEEE Journal of Solid-State Circuits, New York, December 1, 1992,
XP000329041, ISSN: 0018-9200, Vol. 27, No. 12, pp. 1886-1894.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



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CERTIFICATION UNDER 37 C.F.R. § 1.97(e)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

[x] Each item of information contained in the Information Disclosure Statement filed herewith was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

OR

[] No item of information in the Information Disclosure Statement filed, to my knowledge after making reasonable inquiry, was known to any individual designated in 37 C.F.R. § 1.56(c) more


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than three months prior to the filing of this Information
Disclosure Statement.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

November 30, 2004
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